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<b>TRANSMITTAL FORM</b> <small>(to be used for all correspondence after initial filing)</small>	Application / Conf. No.	10/627,335 / 2274
	Filing Date	July 24, 2003
	First Named Inventor	Vincent J. Jorgensen
	Examiner Name	Eun Hee Chung
	Art Unit	2123
	Patent No.	
	Attorney Docket Number	X-1251 US
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X-1251 US  
10/627,335

PATENT  
Conf. No.: 2274



IN THE UNITED STATES PATENT OFFICE

Applicants: Vincent J. Jorgensen et al.  
Assignee: Xilinx, Inc.  
Title: Dummy Block Replacement for Logic Simulation  
Serial No.: 10/627,335 File Date: 07-24-03  
Examiner: Eun Hee Chung Art Unit: 2123  
Docket No.: X-1251 US Conf. No.: 2274

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AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

Dear Sir:

In response to the Office Action mailed from the Patent Office on July 27, 2006,  
please replace/substitute the following claims as indicated.

**Amendments to the Claims** are reflected in the listing of claims which begins  
on page 2 of this paper.

**Remarks** begin on page 6 of this paper.